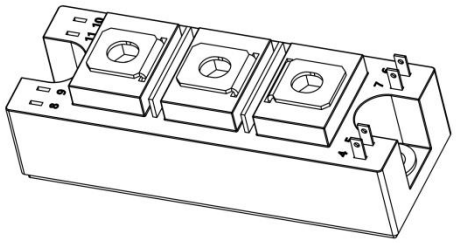
Silicon Carbide (SiC)
1200V 15 mohm
Half-Bridge MOSFET Module
34mm
SC100HB12C2



PIM7, 94.00 × 34.00 × 29.00

The SC100HB12C2 is a power module containing 15 mΩ / 1200 V SiC MOSFET half-bridge and a thermistor with Si_3N_4 AMB in an 34mm package.

Features

- 15 mΩ / 1200 V SiC MOSFET Half-Bridge
- Si_3N_4 AMB
- Low Inductive Layout
- Solderable Pins
- Isolated Base Plate

Typical Applications

- Motor Drives
- Vehicle Fast Chargers
- Renewable energy
- Uninterruptible Power Supply Systems (UPS)

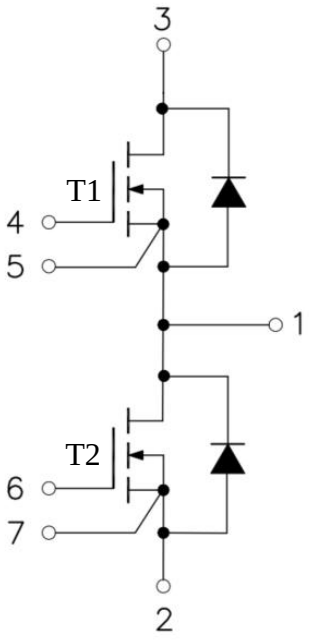
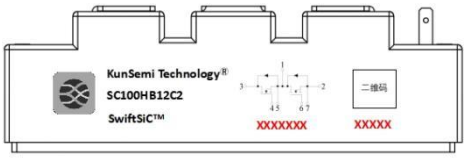


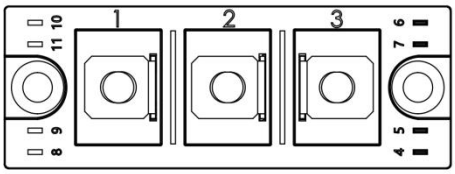
Figure 1. Schematic

MARKING DIAGRAM



SC100HB12C2 = Device Code
XXXXXXX = Year, week and batch Code
XXXXX = Flow code

PIN ASSIGNMENTS



ORDERING INFORMATION

Device	Package
SC100HB12C2	34mm

PIN DESCRIPTION

Pin	Name	Description
1	OUT	Center Point of Half Bridge
2	DC-	DC Negative Bus Connection
3	DC+	DC Positive Bus Connection
4	G1	T1 Gate
5	S1	T1 Source
6	G2	T2 Gate
7	S2	T2 Source

MAXIMUM RATINGS

Rating	Symbol	Value	Unit

SiC MOSFET

Drain−Source Voltage	V_{DSS}	1200	V
Gate−Source Voltage	V_{GS}	+ 22 / - 10	V
Continuous Drain Current @ T_C = 80 °C (T_J = 175 °C)	I_{DS}	100	A
Pulsed Drain Current (T_J = 175°C)	I_{Dpluse}	200	A
Maximum Power Dissipation @ T_C = 80°C (T_J = 175 °C)	P_{tol}	900	W
Minimum Operating Junction Temperature	T_{JMIN}	- 40	°C
Maximum Operating Junction Temperature	T_{JMAX}	175	°C

ELECTRICAL CHARACTERISTICS (T_J = 25 ℃ unless otherwise noted)

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
SiC MOSFET CHARACTERISTICS						
Zero Gate Voltage Drain Current	V_{GS} = 0 V, V_{DS} = 1200 V	I_{DSS}	-	-	1	mA
Drain−Source On Resistance	V_{GS} = 16 V, I_{DS} = 100 A, T_J = 25°C	$R_{DS(ON)}$	-	14.7	-	mΩ
	V_{GS} = 16 V, I_{DS} = 100 A, T_J = 125°C		-	24.3	-	
	V_{GS} = 16 V, I_{DS} = 100 A, T_J = 175°C		-	27.2	-	
Gate−Source Threshold Voltage	V_{GS} = V_{DS}, I_{DS} = 74 mA, T_J = 25°C	$V_{GS(TH)}$	2.10	2.63	3.00	V
Gate Resistance	V_{GS} = 0 V, f = 1 MHz	R_g	-	3.27	-	Ω
Gate Leakage Current	V_{GS} = 22 V,V_{DS} = 0 V	I_{GSS}	-	-	200	nA
Input Capacitance	V_{DS} = 800 V, V_{GS} = 0 V, f = 100 kHz	C_{ISS}	-	5.33	-	nF
Reverse Transfer Capacitance		C_{RSS}	-	22.30	-	pF
Output Capacitance		C_{OSS}	-	0.32	-	nF
Total Gate Charge	V_{DS} = 800 V, V_{GS} = -5 / 20 V, I_D = 100 A	$Q_{G(TOTAL)}$	-	298	-	nC
Gate-Source Charge		Q_{GS}		69		
Gate-Drain Charge		Q_{GD}		154		
Turn−on Delay Time	V_{DS} = 600 V, I_{DS} = 100 A V_{GS} = -5 / 18 V, R_G = 2.35 Ω L_S = 27 uH, T_J = 25 °C	$T_{d(on)}$	-	29.44	-	ns
Rise Time		t_r	-	24.64	-	ns
Turn−off Delay Time		$T_{d(off)}$	-	63.36	-	ns
Fall Time		t_f	-	14.40	-	ns
Turn−on Switching Loss per Pulse		E_{ON}	-	0.30	-	mJ
Turn−off Switching Loss per Pulse		E_{OFF}	-	0.65	-	mJ
Turn−on Delay Time	V_{DS} = 600 V, I_{DS} = 100 A V_{GS} = -5 / 18 V, R_G = 2.35 Ω L_S = 27 uH, T_J = 125 °C	$T_{d(on)}$	-	27.20	-	ns
Rise Time		t_r	-	23.68	-	ns
Turn−off Delay Time		$T_{d(off)}$	-	69.76	-	ns
Fall Time		t_f	-	14.40	-	ns
Turn−on Switching Loss per Pulse		E_{ON}	-	0.30	-	mJ
Turn−off Switching Loss per Pulse		E_{OFF}	-	0.66	-	mJ
Turn−on Delay Time	V_{DS} = 600 V, I_{DS} = 100 A V_{GS} = -5 / 18 V, R_G = 2.35 Ω L_S = 27 uH, T_J = 175 °C	$T_{d(on)}$		25.60		ns
Rise Time		t_r		23.68		ns
Turn−off Delay Time		$T_{d(off)}$		71.36		ns
Fall Time		t_f		13.76		ns
Turn−on Switching Loss per Pulse		E_{ON}		0.31		mJ
Turn−off Switching Loss per Pulse		E_{OFF}		0.67		mJ
Thermal Resistance, Junction to Case (MOSFET)		R_{thJC}	-	0.167	-	°C/W

ELECTRICAL CHARACTERISTICS (T_J = 25℃ unless otherwise noted) (continued)

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
BODY DIODE CHARACTERISTICS						
Diode Forward Voltage	I_{SD} = 100 A, T_J = 25 °C	V_{SD}	-	3.97	-	V
	I_{SD} = 100 A, T_J = 125 °C		-	3.49	-	
	I_{SD} = 100 A, T_J = 175 °C		-	3.44	-	
Diode Reverse Recovery Charge	V_R = 600 V, I_{SD} = 100 A V_{GS} = -5 V, R_G = 2.35 Ω L_S = 27 uH, T_J = 25 °C	Q_r	-	0.87	-	uC
Peak Reverse Recovery Current		I_{rrm}	-	93.95	-	A
Reverse Recovery Time		t_{rr}		15.04		ns
Reverse Recovery Energy		E_{rr}	-	0.57	-	mJ
Diode Reverse Recovery Charge	V_R = 600 V, I_{SD} = 100 A V_{GS} = -5 V, R_G = 2.35 Ω L_S = 27 uH, T_J = 125 °C	Q_r	-	1.65	-	uC
Peak Reverse Recovery Current		I_{rrm}	-	210.87	-	A
Reverse Recovery Time		t_{rr}		17.60		ns
Reverse Recovery Energy		E_{rr}	-	1.23	-	mJ
Diode Reverse Recovery Charge	V_R = 600 V, I_{SD} = 100 A V_{GS} = -5 V, R_G = 2.35 Ω L_S = 27 uH, T_J = 175 °C	Q_r		1.97		uC
Peak Reverse Recovery Current		I_{rrm}		275.68		A
Reverse Recovery Time		t_{rr}		17.92		ns
Reverse Recovery Energy		E_{rr}		1.85		mJ
Thermal Resistance, Junction to Case (Body Diode)		R_{thJC}	-	0.207	-	°C/W

MODULE

Storage Temperature Range	T_{stg}	-40 to 150	°C
Module Operating Junction Temperature	T_J	-40 to 150	°C
Isolation Test Voltage, t = 1 s, 50 Hz	V_{iso}	2500	V
Mounting torque to main terminals(M6 screw)	M_T	6.0	N.m
Mounting torque to main terminals(M5 screw)	M_H	6.0	N.m
Stray Inductance	L_{DS}	13.5	nH
Weight of Module	G	155	g

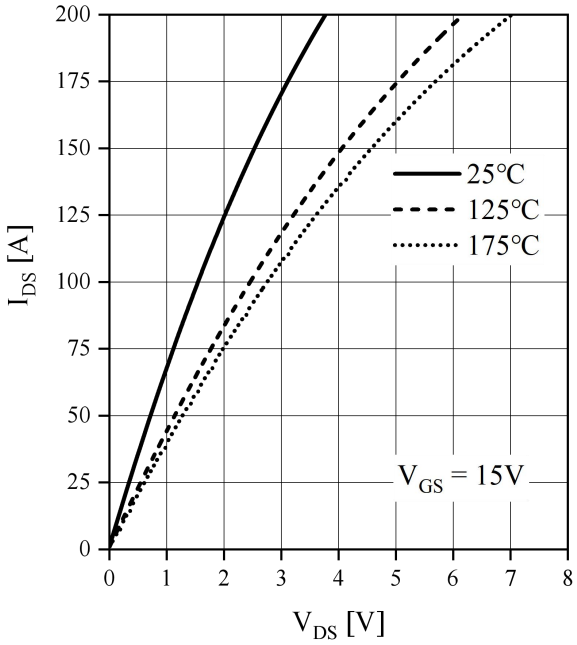


Figure 2. MOSFET Typical Output Characteristic

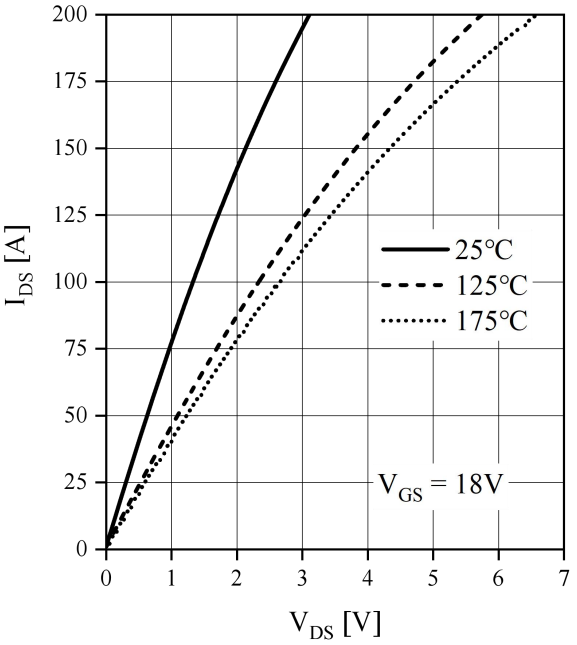


Figure 3. MOSFET Typical Output Characteristic

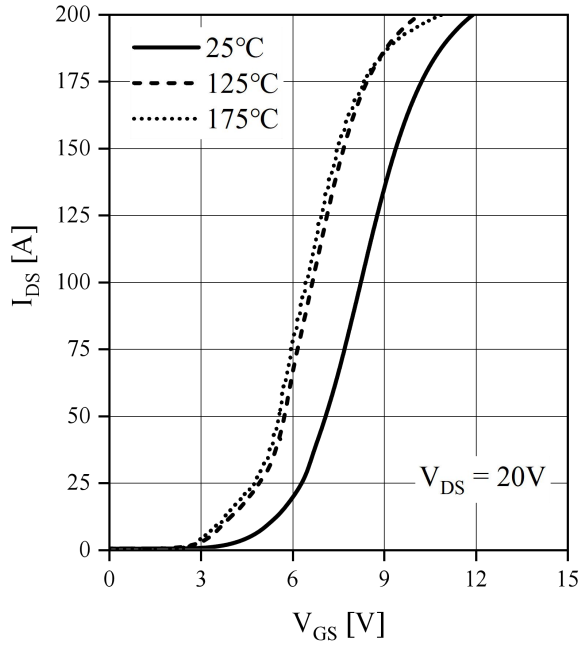


Figure 4. MOSFET Typical Transfer Characteristic

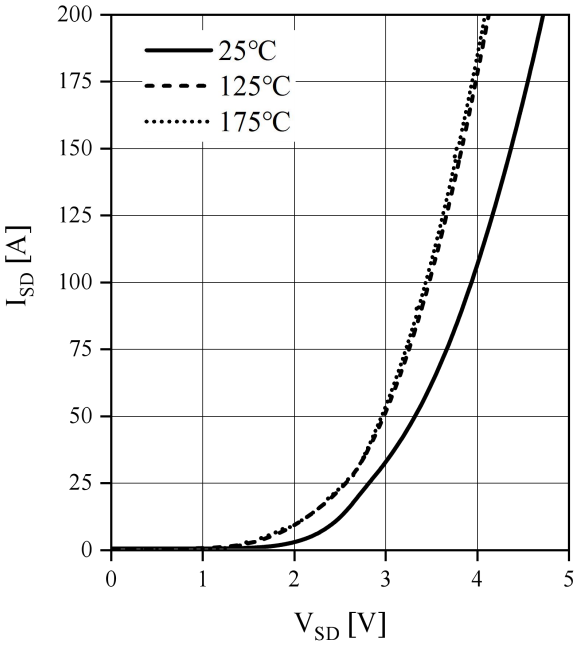


Figure 5. Body Diode Forward Characteristic

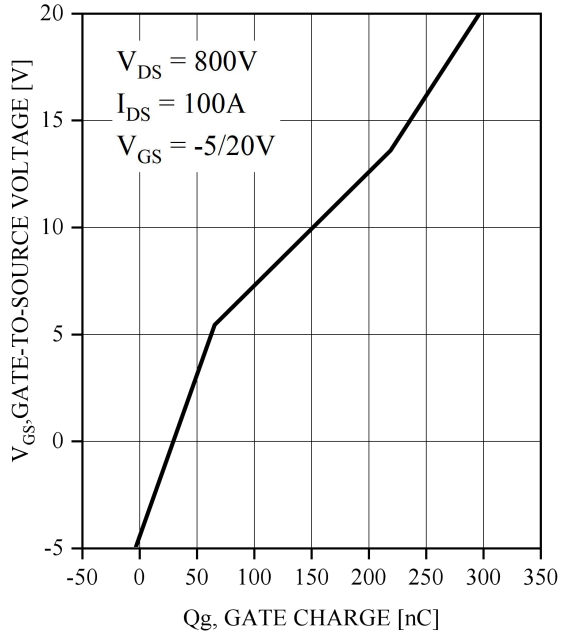


Figure 6. Gate−to−Source Voltage vs. Total Charge

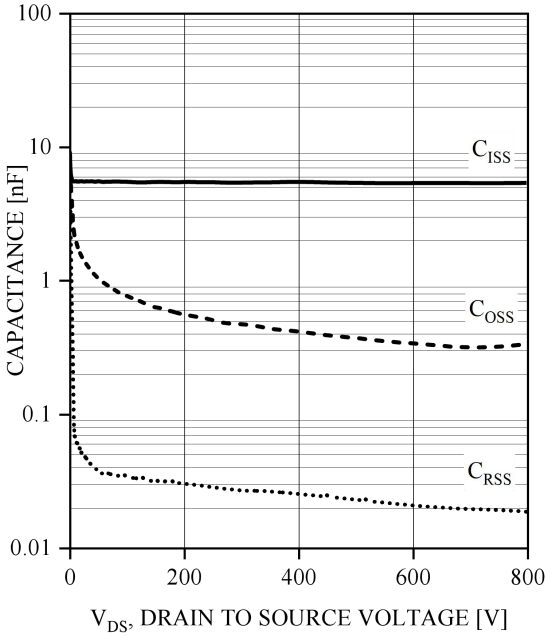


Figure 7. Capacitance vs. Drain−to−Source Voltage

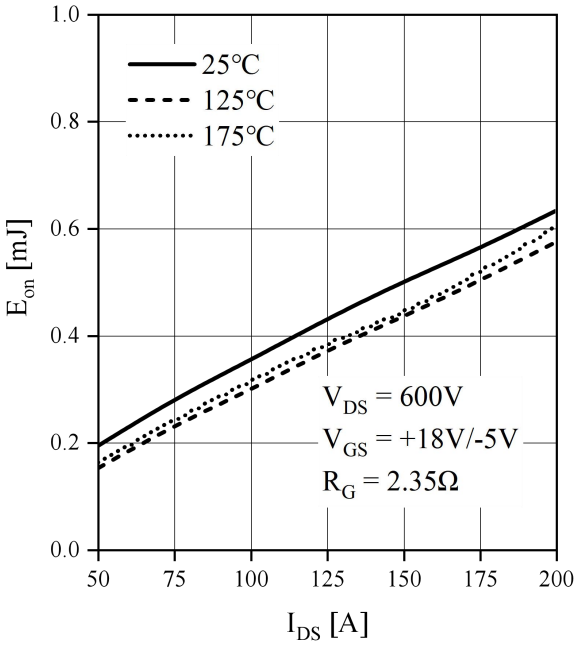


Figure 8. Typical Switching Loss E_{on} vs. I_{DS}

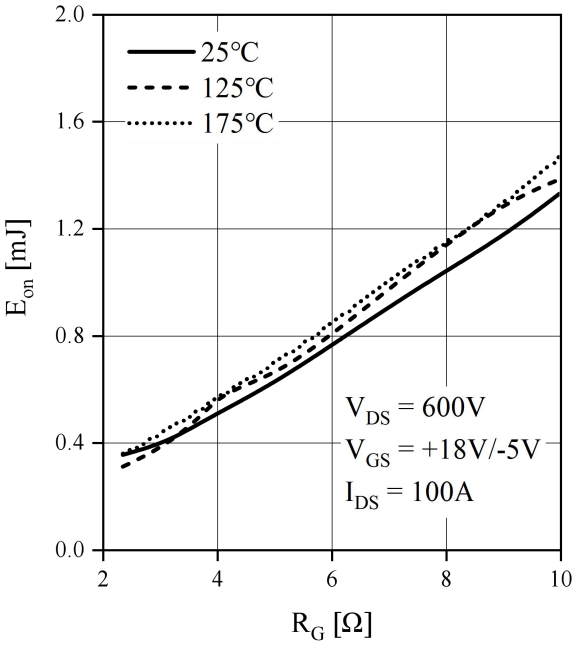


Figure 9. Typical Switching Loss E_{on} vs. R_G

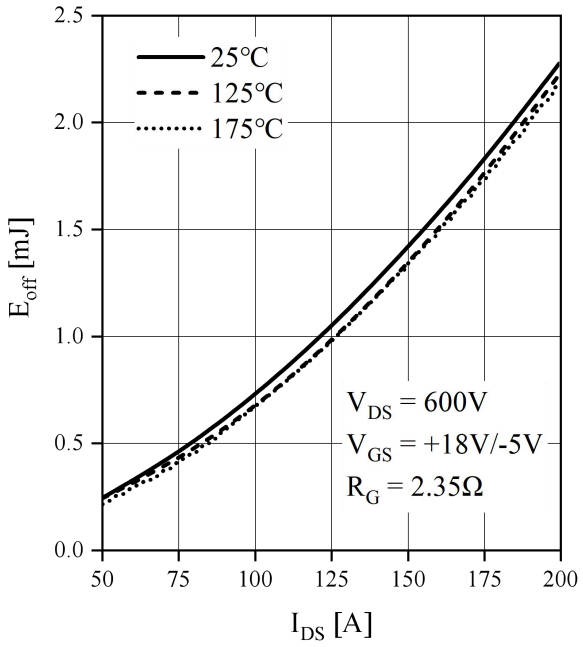


Figure 10. Typical Switching Loss E_{off} vs. I_{DS}

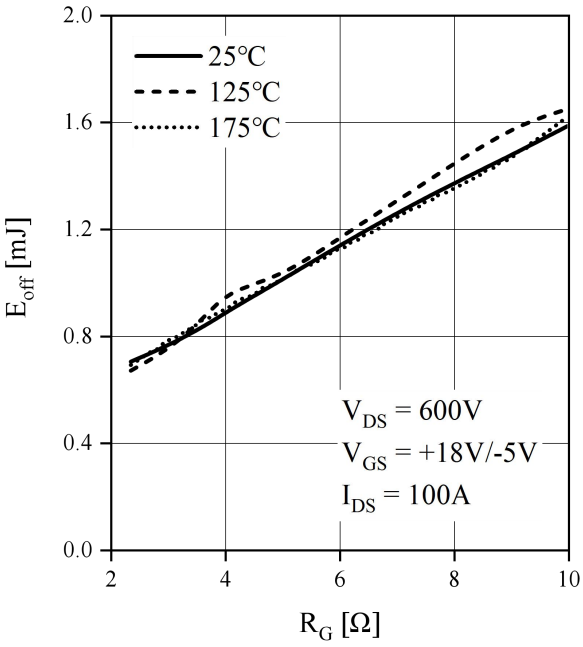


Figure 11.Typical Switching Loss E_{off} vs. R_G

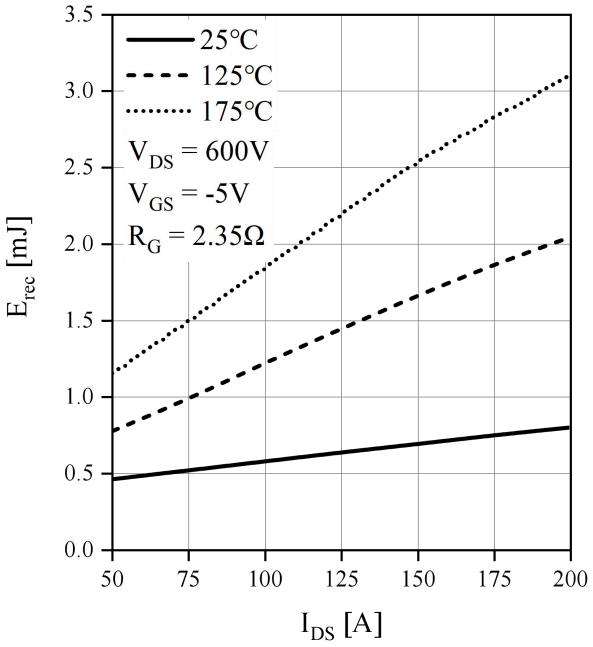


Figure 12. Typical Switching Loss E_{rec} vs. I_{DS}

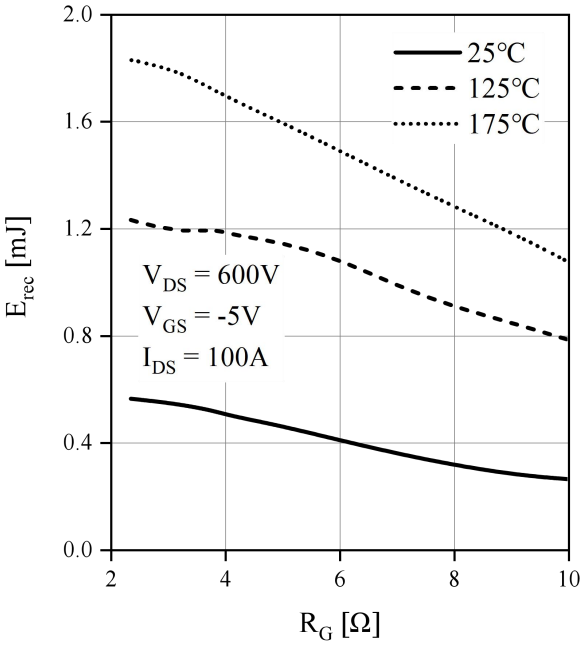


Figure 13. Typical Switching Loss E_{rec} vs. R_G

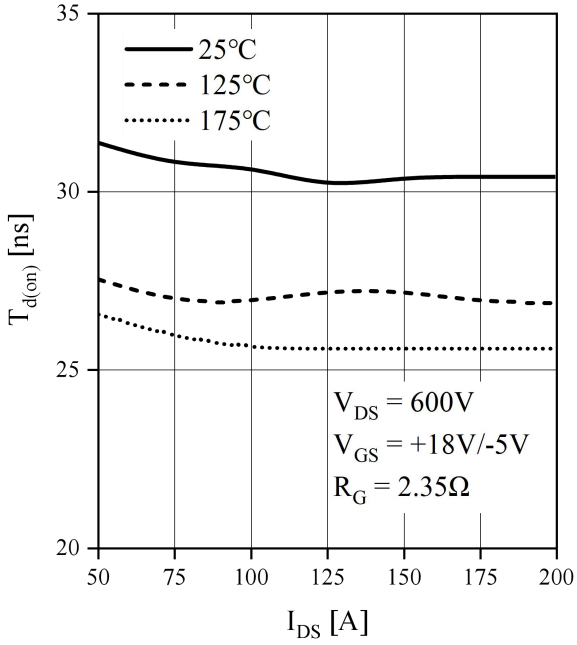


Figure 14. Typical Switching Loss $T_{d(on)}$ vs. I_{DS}

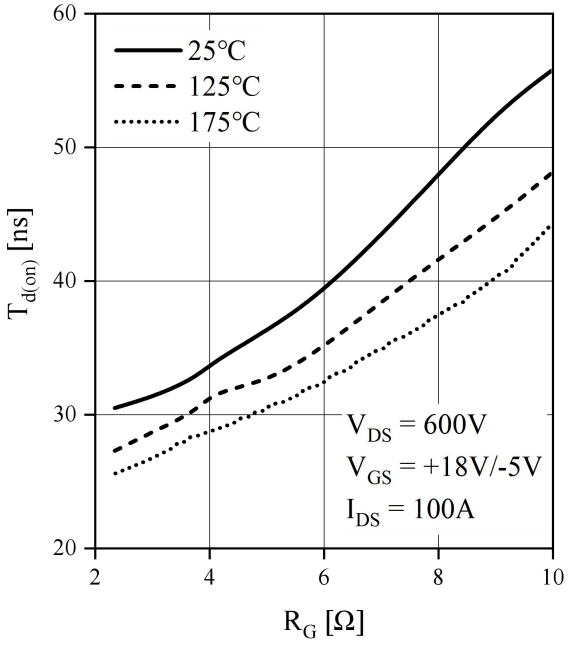


Figure 15. Typical Switching Loss $T_{d(on)}$ vs. R_G

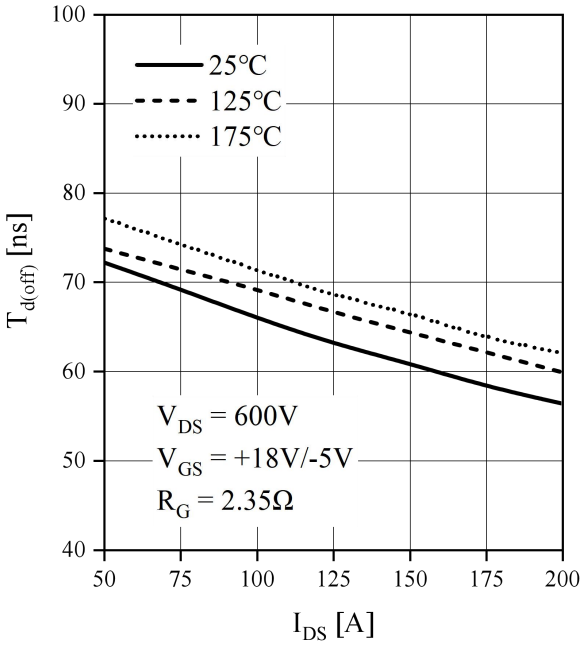


Figure 16. Typical Switching Loss $T_{d(off)}$ vs. I_{DS}

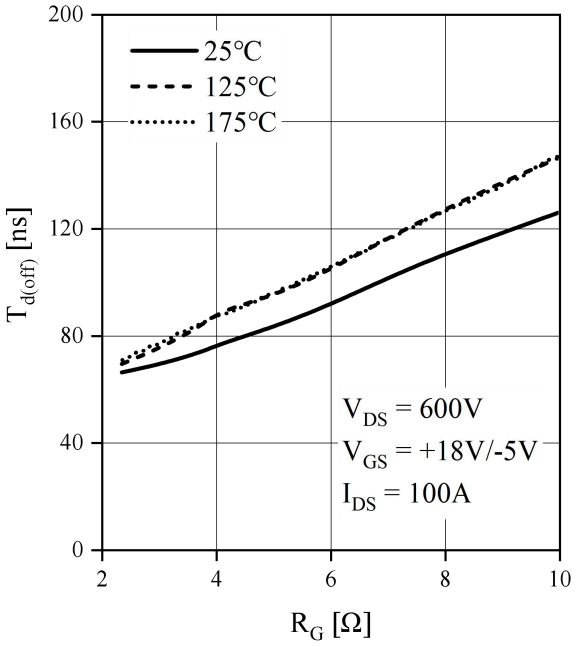


Figure 17. Typical Switching Loss $T_{d(off)}$ vs. R_G

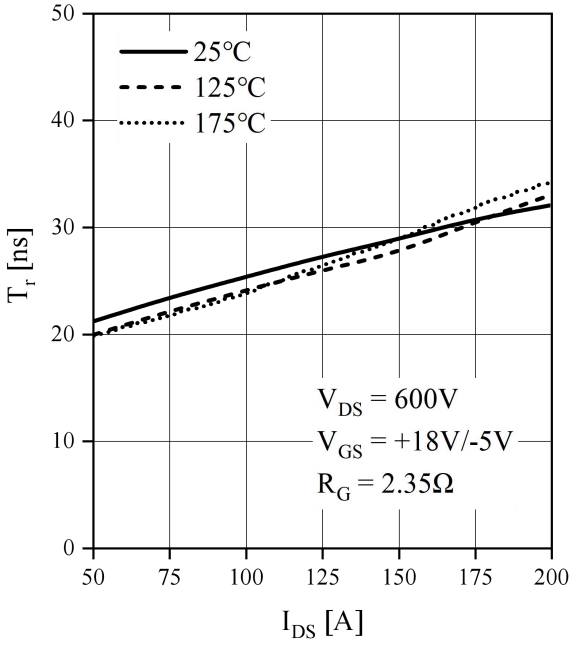


Figure 18. Typical Switching Loss T_r vs. I_{DS}

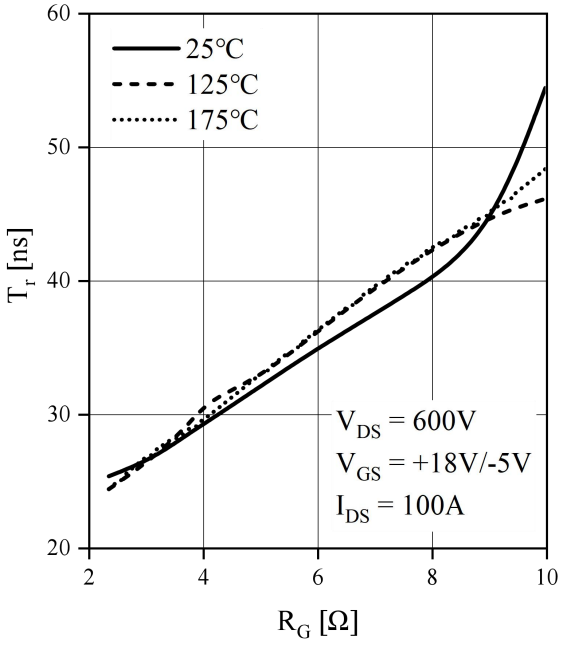


Figure 19. Typical Switching Loss T_r vs. R_G

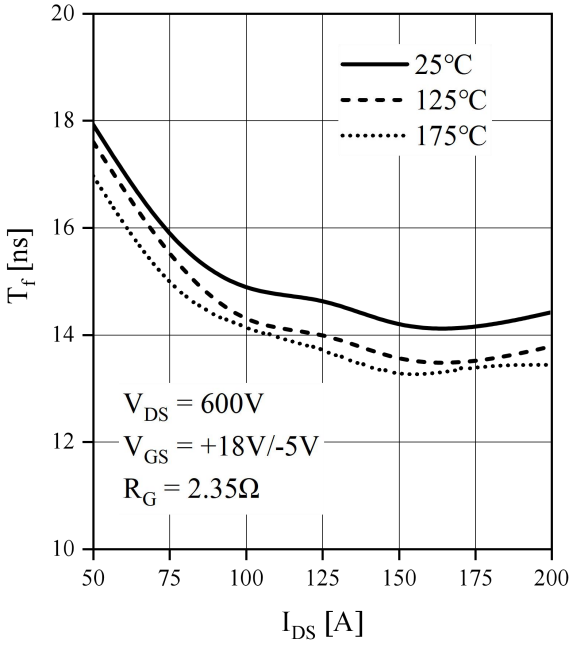


Figure 20. Typical Switching Loss T_f vs. I_{DS}

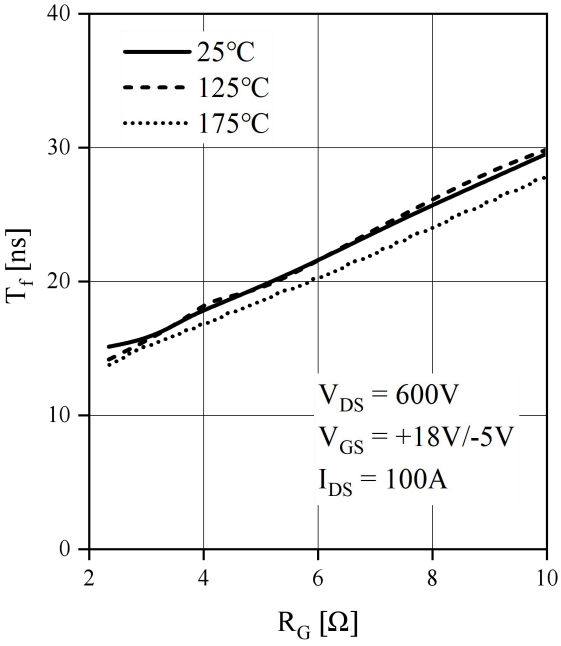


Figure 21. Typical Switching Loss T_f vs. R_G

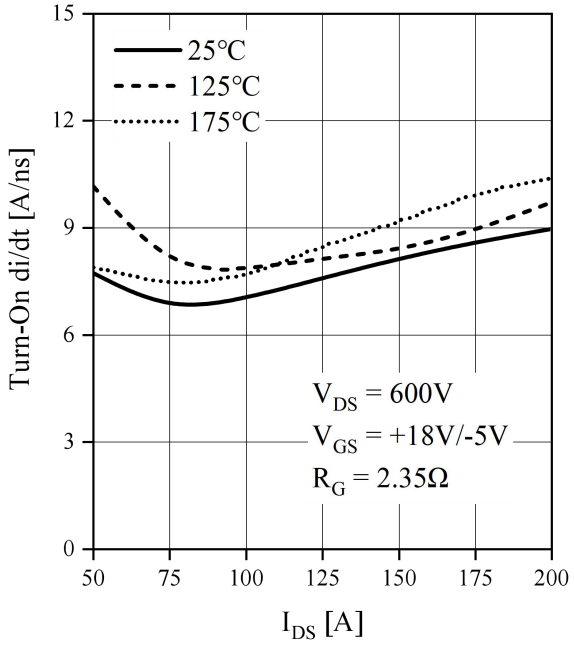


Figure 22. di/dt ON vs. I_{DS}

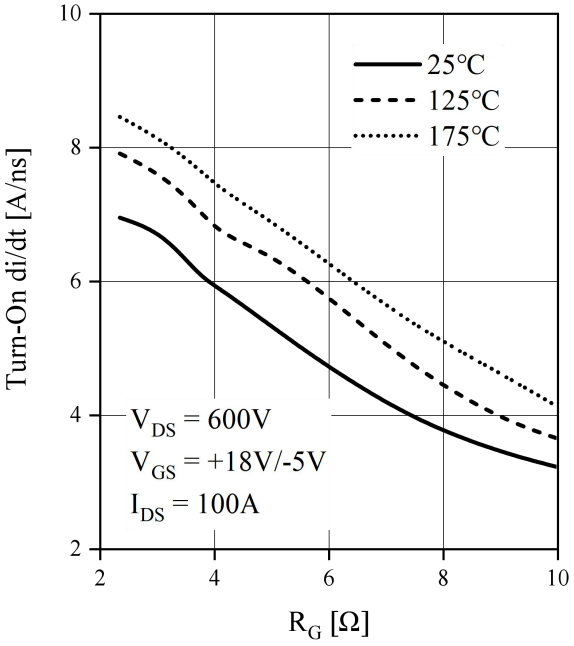


Figure 23. di/dt ON vs. R_G

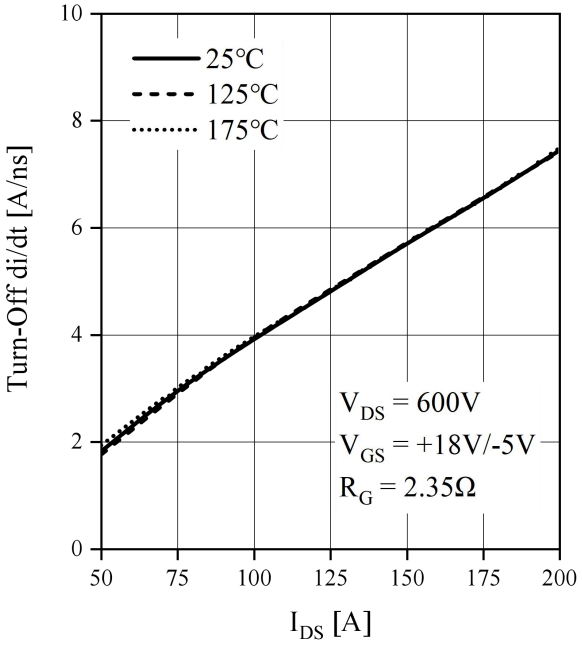


Figure 24. di/dt OFF vs. I_{DS}

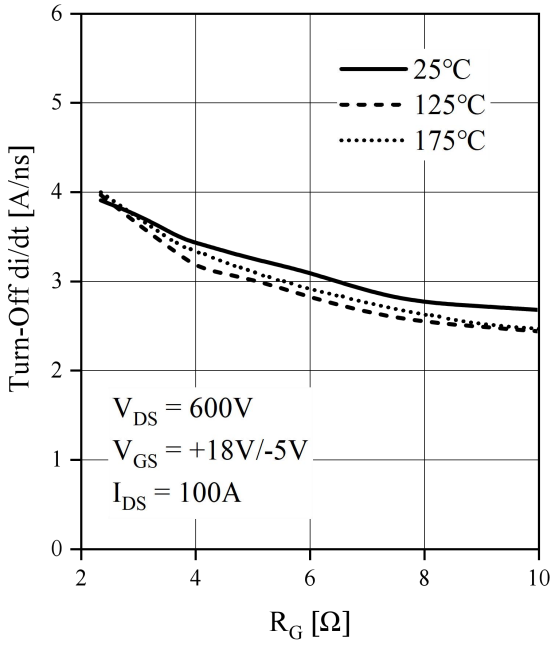


Figure 25. di/dt OFF vs. R_G

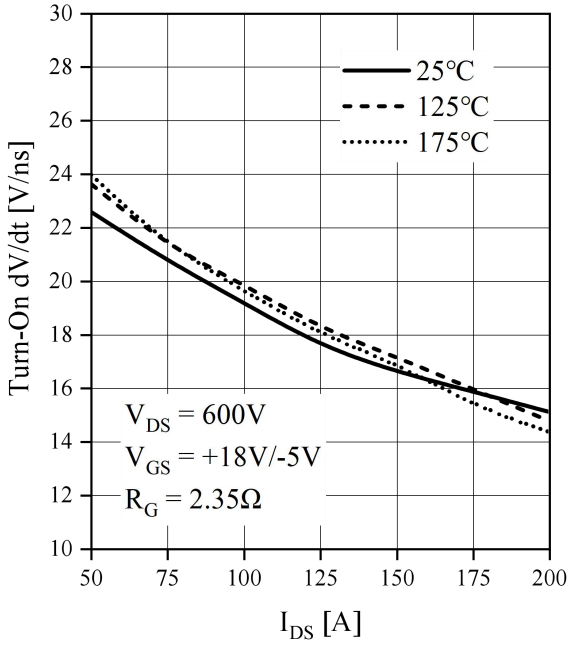


Figure 26. dV/dt ON vs. I_{DS}

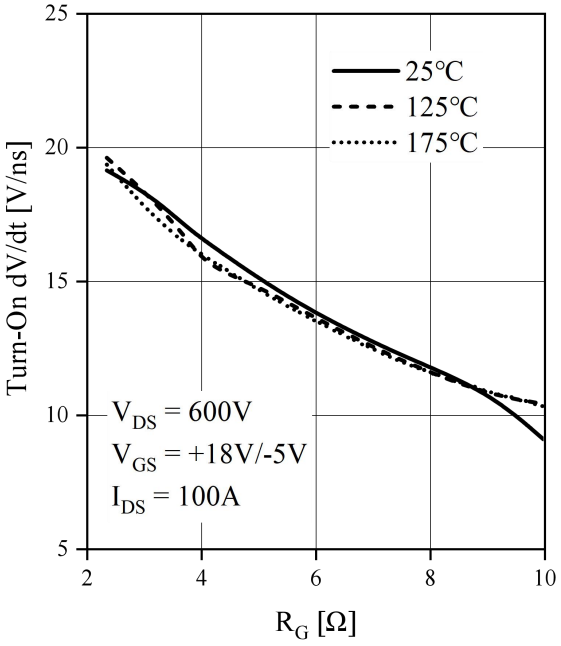


Figure 27. dV/dt ON vs. R_G

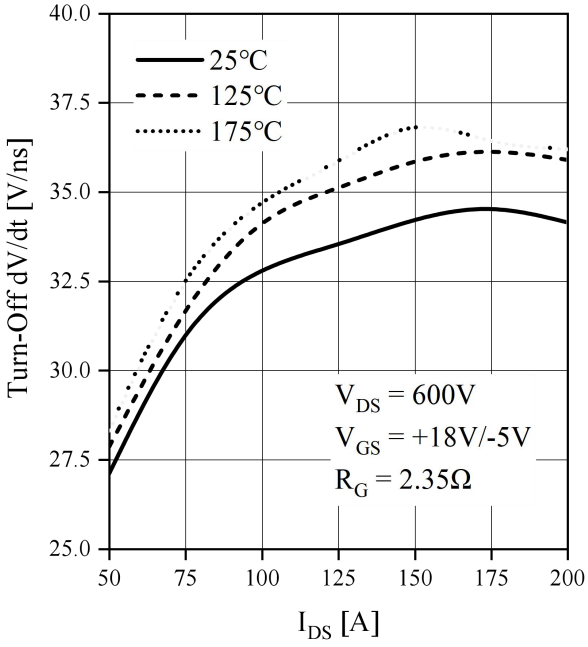


Figure 28. dV/dt OFF vs. I_{DS}

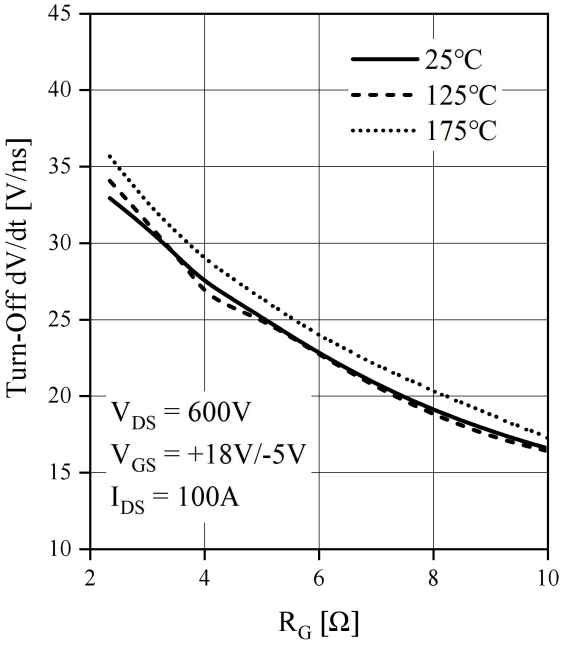


Figure 29. dV/dt OFF vs. R_G

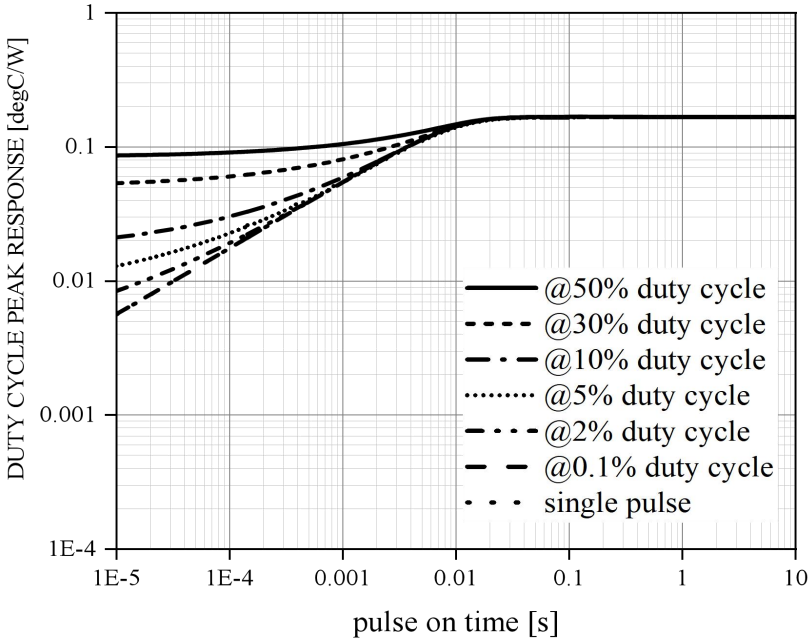
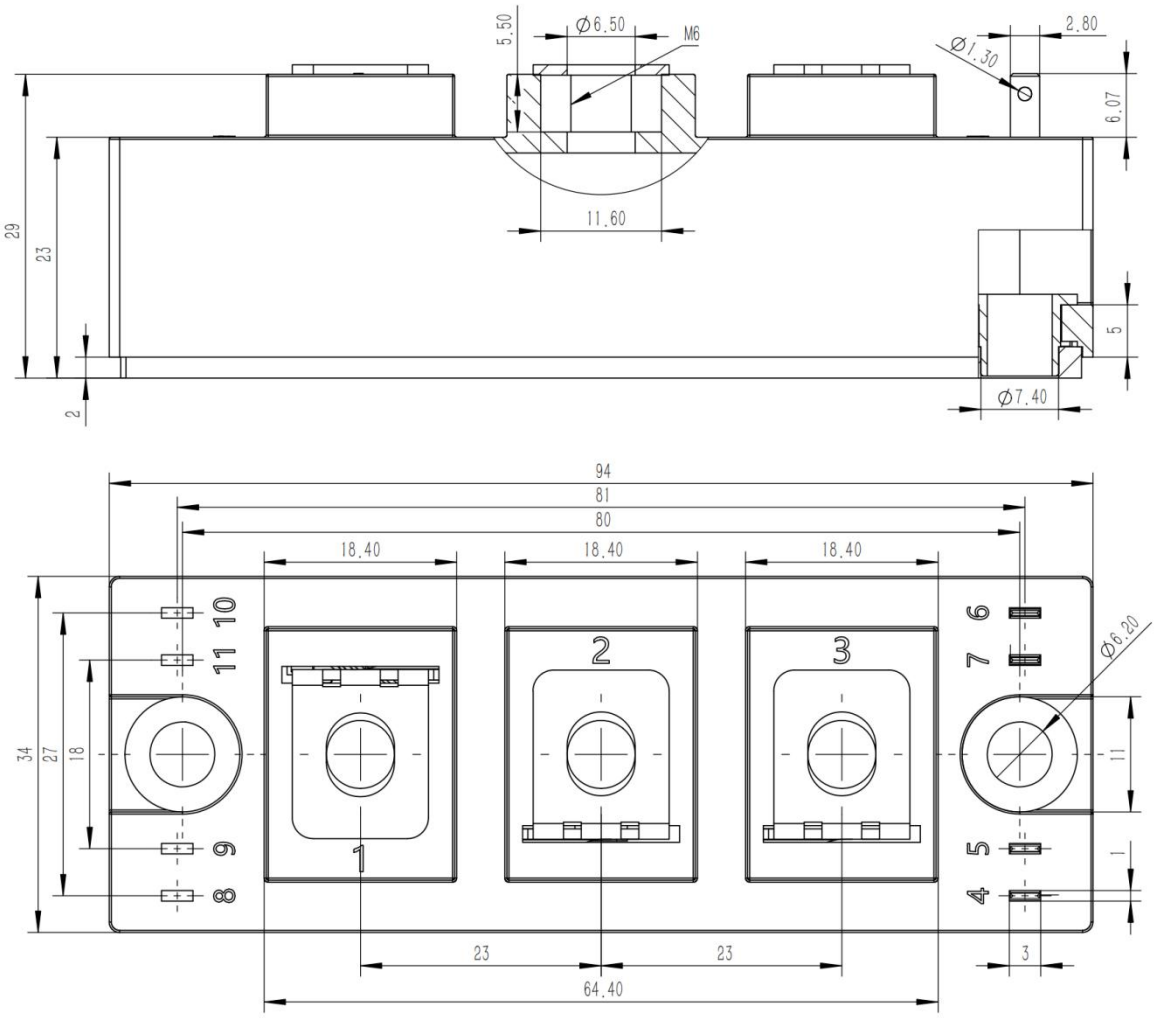


Figure 30. MOSFET Junction−to−Case Transient Thermal Impedance

Package Drawing



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